



# Product Change Notification

## 107551 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

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# Product Change Notification

**Change Notification #:** 107551 - 00  
**Change Title:** Intel® Core™ 2 Duo processor T7700, T7500, L7500, PCN 107551-00, Product Design, E-1 to G-0 Stepping Conversion  
**Date of Publication:** May 24, 2007

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

	<b>T7700 and T7500</b>	<b>L7500</b>
<b>Date of Samples Availability:</b>	Jun 15, 2007	Jul 13, 2007
<b>Date of Qualification Data Availability:</b>	Aug 24, 2007	Aug 24, 2007
<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Sep 07, 2007	Sep 07, 2007
<b>Date of First Availability of Post-Conversion Material:</b>	Jul 27, 2007	Aug 10, 2007

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

The Intel® Core™ 2 Duo processor T7700, T7500, and L7500 4M SKUs on E-1 step will be converted to G-0 step in Q3'07.

- CPUID will change
- No change in terms of electrical, mechanical, and thermal specifications between E-1 step and G-0 step SKUs
- G-0 step incorporates speed path improvements to enable higher frequency bins

Contact your local Intel Corporation field representative for schedules to obtain latest specification information.

## Customer Impact of Change and Recommended Action:

Minimal validation effort for this conversion is expected.

- G-0 stepping will require a BIOS Update.

New QDF#, S-Spec, MM numbers is provided below for G-0 Step Qualification Samples and Production units.

## Products Affected / Intel Ordering Codes:

### Intel® Core(TM)2 Duo processor T7500, T7700 BGA

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
T7700	2.4Ghz	LE80537GG0564M	S LA3M	888428	QXLP	888039	LE80537GG0564M	SLADL	891312
T7500	2.2Ghz	LE80537GG0494M	S LA3N	888430	QXLU	888043	LE80537GG0494M	SLADM	891316

### Intel® Core(TM)2 Duo processor T7500, T7700 PGA

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
T7700	2.4Ghz	LF80537GG0564M	S LA43	888434	QXNM	887969	LF80537GG0564M	SLAF7	891085
T7500	2.2Ghz	LF80537GG0494M	S LA44	888440	QXNR	887972	LF80537GG0494M	SLAF8	891304

### Intel® Core(TM)2 Duo processor L7500

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
L7500	1.6Ghz	LE80537LG0254M	S LA3R	888433	QXMV	888013	LE80537LG0254M	SLAET	891454

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

May 24, 2007

Revision Number:

00

Reason:

Originally Published PCN